

ABSTRACT OF DISCLOSURE

A field-sequential liquid crystal display panel including thin film transistors, each having a drain, a source, and a gate, cell electrodes respectively coupled to the drains or sources of the thin film transistors, scan electrode lines coupled to the gates of the thin film transistors, data electrode lines coupled to the sources or drains of the thin film transistors, and storage capacitors provided between each of the cell electrodes and a respective one of the scan electrode lines, to sustain voltages applied to the cell electrodes.